# BA-4000 Bit Analyzer

## 800G BIT ERROR RATE (BER) TESTER

Electrical BER tester supporting NRZ and PAM4 coding, with advanced FEC tools and with testing capabilities up to 800G.



## **KEY FEATURES**

Supports NRZ and PAM4

Supports PRBS 7/9/11/13/15/23/31/13Q/31Q, SSPRQ

Advanced FEC tools

Supports RS-FEC Scrambled Idle Pattern

Channel simulator

Burst/random error injection

O-SMPM connection

Channel histogram

Channel mapping

Powerful and user-friendly GUI

Automation: API support

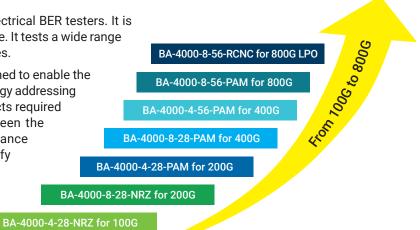
LPO testing supported by RCNC model



#### **BA-4000 READY FOR 800G TESTING**

The BA-4000 is a world-class series of 100G/800G electrical BER testers. It is designed for production line quality control and R&D use. It tests a wide range of devices from components to transceivers and cables.

The BA-4000-RCNC model has been specifically designed to enable the testing of linear-drive pluggable optics (LPO), a technology addressing the need of high bandwidth and low power interconnects required by AI/ML applications. The strong correlation between the BA-4000-RCNC test results and real-switch performance reduces uncertainty, allowing manufacturers to qualify transceivers with confidence and efficiency.



#### POWERFUL AND SIMPLIFIED USER INTERFACE

The BA-4000 graphical user interface (GUI) provides simplified and real-time test results per channel. It requires an external Windows-based PC with Ethernet capability to run the GUI and API.





#### **FEC SIMULATION**

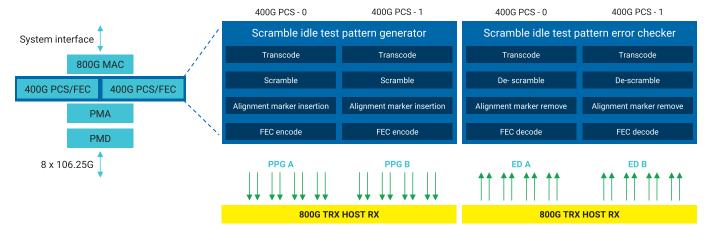
The BER tester includes FEC simulation capabilities. This provides powerful burst error analysis.

Main features include:

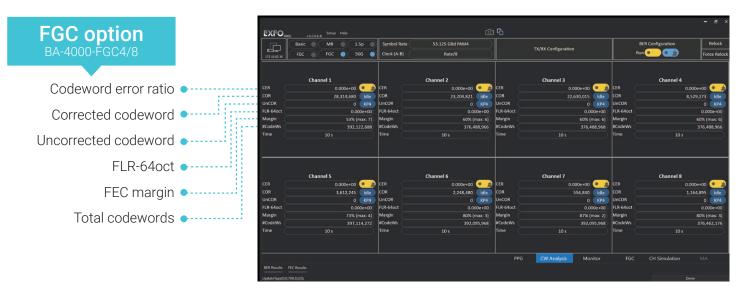
- · PRBS error check and correction
- · Pre-FEC and Post-FEC BER
- · KP4/KR4 and low latency FEC protocols
- · FEC lane striping function
- FEC symbol error distribution plot: codewords vs symbol errors
- · FEC margin auto-calculation

#### FEC encoded scrambled idle

With the FEC Generator and Checker (FGC) option, the BA-4000 addresses RS-FEC scrambled idle pattern for testing 53 GBd host side interfaces as part of the development of new-generation 800G optics, including optical transceivers, DAC, etc.



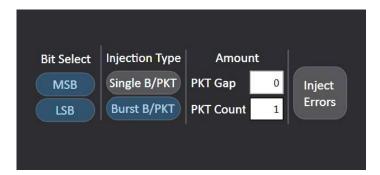
800G function flow structure



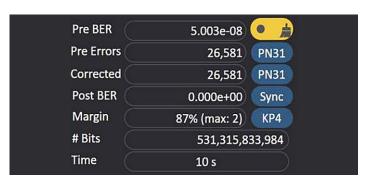
FEC encoded scramble idle metrics in the GUI



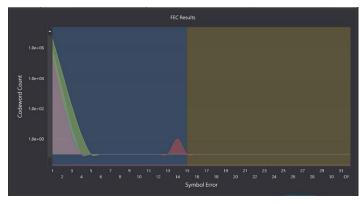
## WITH PAM4 CODING, A SIMPLE BER TEST IS NOT ENOUGH



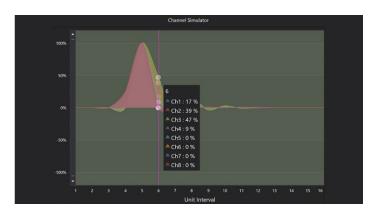
Burst and random error injection



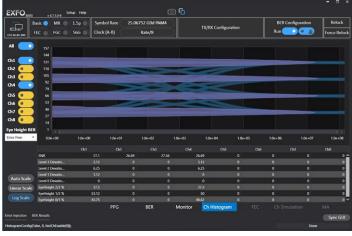
FEC symbol error margin



FEC symbol error distribution plot



Channel response simulation



Channel histogram 7-tap mode





Authono         x-28-NRZ         x-29-PAM         cs-64-PAM-FCDC SSP-PAM-FCDC SSR-PAM-FCDC SSR-PAM-FCDC SSR-PAM-FCDC SSR-PAM-FCDC SSR-PAM-FCDC SSR-PAM-FCDC SSR-PAM-FCDC SSR-PAM-FCDC SSR-PAM-FCDC SSR-PAM-FCDC SSR-PAM-FCDCC SSR-PAM-FCDC SSR-PAM-FCDCC SSR-PAM-FCDCC SSR-PAM-FCDCC SSR-PAM-FCDCC SSR-PAM-FCDCC SSR-PAM-FCDCC SSR-PAM-FCDCC SSR-PAM-FCDCC SSR-PAM-FCDCC SSR-PAM-FCDCC SSR-PAM-FCDCC SSR-PAM-FCDCC SSR-PAM-FCDCC SSR-PAM-FCDCC SSR-PAM-FCDCC SSR-PAM-FCDCC SSR-PAM-FCDCCC SSR-PAM-FCDCCC SSR-PAM-FCDCCC SSR-PAM-FCDCCCC SSR-PAM-FCDCCCC SSR-PAM-FCDCCCC SSR-PAM-FCDCCCCCCC SSR-PAM-FCDCCCCCCC	SPECIFICATIONS					
Modulation   Nicro of Infamines   8 (x = 8)   9 (x =	BA-4000	x-28-NRZ				x-56-RCNC-FECx
Data rate per lane (GRd)	Number of channels			` '		8 (x = 8)
Bata rate   Bata   Ba	Modulation	NRZ only	NRZ/PAM4	NRZ/PAM4	NRZ/PAM4	PAM4 only
Pattern supported by PPG and ED   PRBS 7/9/15/23/31 and pby PPG and ED   PRBS 7/9/11/31/52/31   PRBS 7/9/11/15/23/31   PRBS 7/9/9/11/13/52/31   PRBS 7/9/9/11/3/52/31   PRBS 7/9/9/11/3/52/3/3/31   PRBS 7/9/9/11/3/52/3/3/31   PRBS 7/9/9/11/3/52/3/3/31		10.709, 11.3176, 12, 12.5, 14.025, 21.0, 24.33024, 24.8832, 25, 25.06752, 25.78125, 26.5625, 27.95,	25.78125, 26.5625, 27.95, 28.05, 28.125,	25.78125, 26.5625, 27.95, 28.05, 28.125, 28.9, 29.0625, 49.765, 49.7664, 50, 50.13504, 51.5625, 53.125, 55.9, 55.90747, 56.125, 56.25, 57.8, 58.125,	25.78125, 26.5625, 27.95, 28.05, 28.125, 28.9, 29.0625, 49.765, 49.7664, 50, 50.13504, 51.5625, 53.125, 55.9, 55.90747, 56.125, 56.25, 57.8, 58.125,	n/a
Data rate per lane (Geld)   Data rate per lane (Geld)   Data rate adjustment (ppm)   Data rate adjust		n/a	25.78125, 26.5625, 27.95, 28.05, 28.125,	25.78125, 26.5625, 27.95, 28.05, 28.125, 28.9, 29.0625, 49.765, 49.7664, 50, 50.13504, 51.5625, 53.125, 55.9, 55.90747, 56.125, 56.25, 57.8, 58.125,	25.78125, 26.5625, 27.95, 28.05, 28.125, 28.9, 29.0625, 49.765, 49.7664, 50, 50.13504, 51.5625, 53.125, 55.9, 55.90747, 56.125, 56.25, 57.8, 58.125,	26.5625, 53.125
PRBS 7/9/15/23/31 and by PPG and ED   PRBS 7/9/11/13/15/23/31   PRBS 7/9/11/13/02/37/31   PRBS 7/9/01/11/01/02   PRBS 7/9/01/11/01/02   PRBS 7/9/01/11/01/02   PRBS 7/9/01/11/01/02   PRBS 7/9/01/11/01/02   PRBS 7/9/01/11/01/02   PRBS 7/9/01/01/02   PRBS 7/9/01/01/01/02   PRBS 7/9/01/01/01/01/02   PRBS 7/9/01/01/01/01/01/01/01/01/01/01/01/01/01/	under	n/a	n/a	(support NRZ and PAM4), 51.5625, 53.125	(support NRZ and PAM4), 51.5625, 53.125	n/a
Pattern supported by PPG and ED         PRBS 7/9/15/23/31 and by PPBS 7/9/9/11/3/13/23/31         PRBS 7/9/9/11/3/3/23/31         PRBS 7/9/9/11/3/3/23/31         PRBS 7/9/9/9/11/3/3/3         PRBS 7/9/9/11/3/3/3         PRBS 7/9/9/9/11/3/3/3         9/9/2/3/3/3         3/9/2/3/3/3         3/9/2/3/3/3         3/9/2/3/3/3/3         3/9/2/3/3/3 </td <td></td> <td>0 to ±300</td> <td>0 to ±300</td> <td>0 to ±1000</td> <td>0 to ±1000</td> <td>n/a</td>		0 to ±300	0 to ±300	0 to ±1000	0 to ±1000	n/a
Pattern supported by PPG and ED         PRBS 7/9/15/23/31 and user-defined pattern         PRBS 7/9/11/13/30/11/3130/150/230/31Q         PRBS 7/9/9/11(1/3130/) 150/230/31Q         PRBS 7/9/9/11(1/3120/) 150/230/31Q         PRBS 7/9/9/11(1/3120/) 150/230/31Q         PRBS 7/9/9/11(1/3120/) 150/230/31Q         PRBS 7/9/9/11(1/3120/) 150/31Q         PRBS 7/9/9/11(1/3120/) 150/31Q         PRBS 7/9/9/11(1/3120/) 150/31Q         TX: PRBS 130/150/31Q         PRBS 7/9/9/11(1/3120/) 150/31Q         PRBS 7/9/9/11(1/3120/) 150/31Q         TX: PRBS 130/150/31Q         TX: PRBS				PRBS 7/9/11/13/15/23/31	PRBS 7/9/11/13/15/23/31	
Pattern supported by PPG and ED under FEC mode         PRBS 7/9/11/15/23/31 23/31         PRBS 7/9/11/15/23/31 PRBS 70/9Q/11Q/15Q/23/31         PRBS 70/9Q/11Q/15Q/33/31 PRBS 70/9Q/11Q/15Q/23/31         This PRBS 13Q/15 SSPRQ 23Q/31Q user-defined pattern Rs: PRBS 15Q/31Q. SSPRQ 23Q/31Q user-defined pattern Rs: PRBS 15Q/31Q. SSPRQ 23Q/31Q user-defined pattern Rs: PRBS 15Q/31Q. SSPRQ 23Q/31Q user-defined pattern Rs: PRBS 15Q/31Q			PRBS 7Q/9Q/11Q/13Q/ 15Q/23Q/31Q Only PPG supports PRBS16Q, SSPRQ, user-	PRBS 7Q/9Q/11Q/13Q/ 15Q/23Q/31Q Only PPG supports PRBS16Q, SSPRQ,	PRBS 7Q/9Q/11Q/13Q/ 15Q/23Q/31Q Only PPG supports PRBS16Q, SSPRQ,	n/a
amplitude (mV <sub>ped</sub> )         800°C         800°C <td>and ED</td> <td>n/a</td> <td>23/31 PRBS 7Q/9Q/11Q/</td> <td>PRBS 7Q/9Q/11Q/15Q/</td> <td>PRBS 7Q/9Q/11Q/15Q/</td> <td>15Q/31Q, SSPRQ, user-defined pattern</td>	and ED	n/a	23/31 PRBS 7Q/9Q/11Q/	PRBS 7Q/9Q/11Q/15Q/	PRBS 7Q/9Q/11Q/15Q/	15Q/31Q, SSPRQ, user-defined pattern
10/10 c   25.78125G   23 d   26.5625G   25 d		800 b, c	800 c, e, j	800 f, j	800 f, j	800 <sup>f, j</sup>
(ps)         n/a         23° (26.5625G)         23 ° (26.5625G)         200 ° (53.125G) 450 ° (25.78125G)         n/a         n/a         n/a         n/a         n/a         Auto-adaptive         D10 (PAM4 53.125G)         150 ° (PAM4 53.125G)         150 ° (PAM4 53.125G)         150 ° (PAM4 53.125G)         200 ° (50.78125G)         150 ° (PAM4 53.125G)         15		16.5/16.5°	11/11°			n/a
Sensitivity (mV <sub>ppp</sub> ) h 100 (NRZ 25.78125G) 200 (PAM4 26.5625G) 250 (PAM4 53.125G) 200 i.m (PAM4 53.125G) 150 (PAM4 53.125G) 15		n/a	23 <sup>d</sup>			
CTLE (dB) 0 to 7 0 to 8 n/a n/a n/a Auto-adaptive ED damage level (mV <sub>pol</sub> ) 1200 1200 1200 1200 1200 1200 900 1200 12	Jitter RMS (fs)	750°	450°			n/a
ED damage level (mV <sub>ppd</sub> ) 1200 1200 1200 1200 1200 900  Clock ratio (clock frequency / symbol rate) /8, /16	Sensitivity (mV <sub>ppd</sub> ) <sup>h</sup>	100 (NRZ 25.78125G)	200 (PAM4 26.5625G)	250 <sup>1</sup> (PAM4 53.125G)	200 <sup>i, m</sup> (PAM4 53.125G)	150 <sup>1</sup> (PAM4 53.125G)
Clock ratio (clock frequency / 8, / 16 / 2, / 4, / 8, / 16, / 32, / 64 / 2, / 4, / 8, / 16, / 32, / 64 / 2, / 4, / 8, / 16, / 32, / 64 / 8, / 16, / 32, / 64  Connector type  Supported	CTLE (dB)	0 to 7	0 to 8	n/a	n/a	Auto-adaptive
/ symbol rate) / 8,716 /2,74,78,716,732,764 /2,74,78,716,732,764 /2,74,78,716,732,764 /2,74,78,716,732,764 /8,716,732,764  Connector type	ED damage level (mV <sub>ppd</sub> )	1200	1200	1200	1200	900
BER monitor  Supported  Supported  Supported  Supported  Supported  (FEC option required)  Channel simulation  Supported  (FEC option required)  Supported		/8, /16	/2, /4, /8, /16, /32, /64	/2, /4, /8, /16, /32, /64	/2, /4, /8, /16, /32, /64	/8, /16, /32, /64
FEC plot n/a Supported (FEC option required) Supported Supported Supported Channel histogram n/a Supported	Connector type		O-SMPM	connector (up to 67 GHz band)	width)	
Channel histogram n/a (FEC option required) Supported Supported Supported Supported Channel simulation n/a Supported	BER monitor	Supported	Supported	Supported	Supported	Supported
Channel simulation n/a Supported (FEC option required) Supported Supported Supported	FEC plot	n/a		Supported	Supported	Supported
Channel simulation n/a (FEC option required) Supported Supported Supported	Channel histogram	n/a	Supported	Supported	Supported	Supported
Error injection Supported Supported Supported Supported n/a	Channel simulation	n/a		Supported	Supported	Supported
	Error injection	Supported	Supported	Supported	Supported	n/a

- a. Fixed rate.
- b. Amplitude step is 200  $\mathrm{mV}_{\mathrm{ppd}}$
- c. NRZ 25.78125 GBd signal measured by 50 GHz bandwidth scope with 40 GHz 2.92 mm, 15 cm RF cable.
- d. PAM4 26.5625 GBd signal measured by 50 GHz bandwidth scope with 40 GHz 2.92 mm, 15 cm RF cable.
- e. NRZ 53.125 GBd signal measured by 50 GHz bandwidth scope with 50 GHz 2.4 mm, 15 cm RF cable. Post-cursor is -2%.
- f. PAM4 53.125 GBd signal measured by 50 GHz bandwidth scope with 50 GHz 2.4 mm, 15 cm RF cable. Post-cursor is -2%.
- g. PAM4 53.125 GBd signal measured by 50 GHz bandwidth scope with 50 GHz 2.4 mm, 15 cm RF cable. Post-cursor is 0%.

- h. Measured by direct loopback from PPG to ED with 40 GHz O-SMPM, 20 cm RF cable.
- i. BER  $\leq 10^{-10}$
- j. Support overdrive 900 m $V_{\rm ppd}$
- k. Use GUI version 6.17 or newer.
- I. Under FEC mode, no support of PRBS13Q, PRBS16Q, SSPRQ, and user-defined pattern at ED. m. Receiving range is up to 500 mV  $_{ppt}$  and BER  $\leq 10^{-10}$



GENERAL SP	ECIFICATIONS	
Size (H x W x D	))	103 mm x 442 mm x 300 mm (4.1 in x 17.4 in x 11.8 in)
Weight		≤ 10 kg (22 lb)
Temperature	Operating Storage	5 °C to 40 °C (41 °F to 104 °F) -20 °C to 70 °C (-4 °F to 158 °F)
Relative humid	ity	20% to 80%
Power		100 Vac to 240 Vac (47 Hz to 63 Hz) 60 W typical / 80 W max.

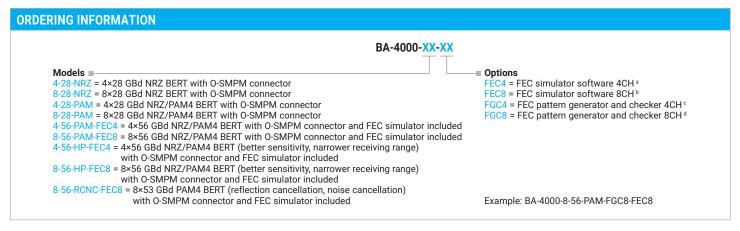
## **COMPARISON TABLE**

	BA-4000-8-56-PAM-FEC8	BA-4000-8-56-RCNC-FEC8	
Description	8×56 GBd NRZ/PAM4 BERT, FEC simulator included BA-4000-8-56-PAM-FEC8	8×53 GBd PAM4 BERT (reflection cancellation, noise cancellation), FEC simulator included	
Applications	DSP-based transceivers and cables	LPO and DSP-based transceivers	
Modulation	NRZ/PAM4	PAM4 only	
Data rate per lane (GBd) under FEC mode	24.8832, 25, 25.06752, 25.78125, 26.5625, 27.95, 28.05, 28.125, 28.9, 29.0625, 49.765, 49.7664, 50, 50.13504, 51.5625, 53.125, 55.9, 55.90747, 56.125, 56.25, 57.8, 58.125, 59.375	26.5625, 53.125	
Data rate adjustment (ppm)	0 to ±1000	n/a	
Pattern supported by PPG and ED under FEC mode	PRBS 7/9/11/15/23/31 PRBS 7Q/9Q/11Q/15Q/23Q/31Q Only PPG supports PRBS16Q, SSPRQ, and user-defined pattern	PPG: PRBS 13Q/15Q/31Q, SSPRQ, user-defined pattern ED: PRBS 15Q/31Q	
Eye width (zero hit) (ps)	5.5 (PAM4 53.125G) 23 (PAM4 26.5625G)	6 (PAM4 53.125G) 23 (PAM4 26.5625G)	
Sensitivity (mV <sub>ppd</sub> ) @ BER ≤ e-10	250 (PAM4 53.125G)	150 (PAM4 53.125G)	
CTLE (dB)	n/a	Auto-adaptive	
FFE taps	16	20	
RCNC (reflection cancellation, noise cancellation)	n/a	Supported	
ED damage level (mV <sub>ppd</sub> )	1200	900	
Clock output amplitude (mV <sub>ppd</sub> )	400	300	
Clock ratio (clock frequency / symbol rate)	/) // // // // // // // // // // // // /		
Error injection Supported		n/a	

## **AVAILABLE OPTIONS**

BA-4000	FEC4	FEC8	FGC4	FGC8
4-28-NRZ				
8-28-NRZ				
4-28-PAM	<b>✓</b>			
8-28-PAM		<b>✓</b>		
4-56-PAM-FEC4			<b>✓</b>	
8-56-PAM-FEC8				<b>✓</b>
4-56-HP-FEC4			<b>✓</b>	
8-56-HP-FEC8				<b>✓</b>
8-56-RCNC-FEC8				





- a. Available for BA-4000-4-28-PAM.
- b. Available for BA-4000-8-28-PAM.
- c. Available for BA-4000-4-56-PAM-FEC4 and BA-4000-4-56-HP-FEC4.
- d. Available for BA-4000-8-56-PAM-FEC8 and BA-4000-8-56-HP-FEC8.

#### **MODEL LIST**

FULL MODEL NO.	DESCRIPTION
BA-4000-4-28-NRZ	4×28G NRZ BERT
BA-4000-8-28-NRZ	8×28G NRZ BERT
BA-4000-4-28-PAM	4×28G NRZ/PAM4 BERT
BA-4000-4-28-PAM-FEC4	4×28G NRZ/PAM4 BERT with FEC simulator
BA-4000-8-28-PAM	8×28G NRZ/PAM4 BERT
BA-4000-8-28-PAM-FEC8	8×28G NRZ/PAM4 BERT with FEC simulator
BA-4000-4-56-PAM-FEC4	4×56G NRZ/PAM4 BERT with FEC simulator
BA-4000-4-56-PAM-FGC4-FEC4	4×56G NRZ/PAM4 BERT with FEC pattern generator and checker and FEC simulator
BA-4000-8-56-PAM-FEC8	8×56G NRZ/PAM4 BERT with FEC simulator
BA-4000-8-56-PAM-FGC8-FEC8	8×56G NRZ/PAM4 BERT with FEC pattern generator and checker and FEC simulator
BA-4000-4-56-HP-FEC4	4×56G NRZ/PAM4 BERT (better sensitivity, narrower receiving range) with FEC simulator
BA-4000-4-56-HP-FGC4-FEC4	4×56G NRZ/PAM4 BERT (better sensitivity, narrower receiving range) with FEC pattern generator and checker, and FEC simulator
BA-4000-8-56-HP-FEC8	8×56G NRZ/PAM4 BERT (better sensitivity, narrower receiving range) with FEC simulator
BA-4000-8-56-HP-FGC8-FEC8	8×56G NRZ/PAM4 BERT (better sensitivity, narrower receiving range) with FEC pattern generator and checker, and FEC simulator
BA-4000-8-56-RCNC-FEC8	8×53G PAM4 BERT (reflection and noise cancellation) with FEC simulator

**EXFO** headquarters T+1 418 683-0211 Toll-free +1 800 663-3936 (USA and Canada)

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